

FIGS. 10A and 10B are explanatory diagrams showing the interpolating operation of the DSP in FIG. 1;

FIGS. 11A and 11B are diagrams for explaining interpolation functions;

On page 9, paragraph 1 change as follows:

FIGS. 12A and 12B are diagrams showing interpolating operation by an interpolation apparatus;

On page 9, paragraph 5 change as follows:

FIGS. 16A and 16B are diagrams showing another embodiment of interpolating operation by a curve;

On page 9, paragraph 8 change as follows:

FIGS. 19A and 19B are diagrams showing an embodiment of 20 interpolating operation by a straight line;

On page 9, paragraph 10 change as follows:

FIGS. 21A and 21B are diagrams showing another embodiment of 25 interpolating operation by a straight line;

On page 10, paragraph 2 change as follows:

FIGS. 23A-23C are diagrams for explaining the interpolating operation of a conventional variable low pass filter style;

On page 10, paragraph 3 change as follows:

FIGS. 24A and 24B are diagrams for explaining the interpolating operation of a conventional variable low pass filter style;

On page 10, paragraph 5 change as follows:

FIGS. 26A and 26B are fundamental block diagrams showing the preliminary treatment and interpolation processing of a digital image signal;

On page 10, paragraph 6 change as follows:

FIGS. 27A-27C are conceptual diagrams of the transforming of a series of pixels in FIG. 26;

On page 10, paragraph 8 change as follows:

FIGS. 29A-29G are timing charts showing the operation of the circuit in FIG. 28; and

On page 10, paragraph 9 change as follows:

FIGS. 30A-30C are drawings showing a transversal filter executing the two-dimensional interpolation processing of an image signal.